

5-10-05



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Olof Tornblad et al.
Serial No.: 10/678,837
Filed: October 3, 2003
Group Art Unit: 2811
Examiner: Ida M. Soward.
Title: LDMOS Transistor

MAIL STOP - AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Number **EV351262321US** in an envelope addressed to: Mail Box Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

alexandria, VA 22313-1450.
Angela Sodine

Angela Loding

Page _____

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the reference listed on the attached PTO-1449 form be considered and cited in the examination of the above-referenced patent application. A copy of the reference is enclosed for the Examiner's convenience. Applicants submit that the item contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that this reference is material to the patentability of the present application.

05/11/2005 SMINASS1 00000053 10678837

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Applicant encloses a check in the amount of \$180.00 however, the Commissioner is hereby authorized to charge any additional fees or credit any overpayments to Deposit Account No. 02-0383 of Baker Botts L.L.P.

Date: May 9, 2005

By: 
Andreas H. Grubert
(Limited recognition 37 C.F.R. §10.9)
AGENT FOR APPLICANTS

SEND CORRESPONDENCE TO:

BAKER BOTT S L.L.P.

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512.322.2545

512.322.8319 (Fax)

PTO-1449 Information Disclosure Citation in an Application			Application No.	Applicant(s)		
			10/678,837	Tornblad et al.		
			Docket Number	Group Art Unit	Filing Date	
			068736.0231	2811	October 3, 2003	
MAY 09 2005 U.S. PATENT DOCUMENTS						
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.	5132753	7/21/92	Chang et al.	357	23.4	3/23/90
B.	6613622	9/2/03	Nair et al.	438	188	7/15/02
C.						
D.						
E.						
F.						
G.						
H.						
I.						
J.						
K.						
L.						
M.						
N.						
O.						
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES
P.						NO
Q.						
R.						
S.						
T.						
U.						
NON-PATENT DOCUMENTS - DOCUMENT (Including Author, Title, Source, and Pertinent Pages)						
V.	Dragon et al., "A Silicon MOS Process for Integrated RF Power Amplifiers," IEEE MTT-S Digest, Pages 257-260				6/17/96	
W.						
X.						
Y.						
Z.						
EXAMINER				DATE CONSIDERED		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						